

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination	
		09/541,399	WU ET AL.	
Examiner		William H. Wood	Art Unit	Page 1 of 1
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	B	US-5,894,576	04-1999	Bharadwaj, Jayashankar	717/156
	C	US-6,108,698	08-2000	Tenev et al.	709/220
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NON-PATENT DOCUMENTS

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	U	Steven S. Muchnick; "Advanced Compiler Design implementation"; Morgan Kaufmann Publishers, Inc.; 1997; pp. 169-177.
	V	Douglas G. Fritz; "An Overview of Hierarchical Control Flow Graph Models"; Proceedings of the 1995 Winter Simulation Conference; 1995; pp. 1347-1355.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.